



		,		
-	1545451	processor core\$1	USPAT; US-PGPUB;	2004/01/26 14:43
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	1248957	TAP controller\$1	USPAT;	2004/01/26 14:43
			US-PGPUB;	, ==,==
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	6006636	TAP core control logic	USPAT;	2004/01/26 14:44
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
- ,	5563037	TAP control register\$!	USPAT;	2004/01/26 14:44
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	5943593	TAP control cwitch	IBM_TDB	2004/01/26 14:44
-) 25422A	TAP control switch	USPAT; US-PGPUB;	2004/01/26 14:44
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	344	non-core	USPAT;	2004/01/26 14:45
	3	Tion core	US-PGPUB;	2001/01/2011.15
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	866075	core\$1	USPAT;	2004/01/26 14:45
			US-PGPUB;	
			EPO; JPO;	
		·	DERWENT;	
			IBM_TDB	
-	1545451	core\$1 or (processor core\$1)	USPAT;	2004/01/26 14:45
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	94957	(core\$1 or (processor core\$1)) with (TAP controller\$1)	IBM_TDB USPAT;	2004/01/26 14:46
-	/CEPE	(corest or (brocessor corest)) Mini (TAL controllerst)	US-PGPUB;	2007/01/20 14.40
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5275310	(TAP core control logic) with (TAP control register\$!) with (TAP	USPAT;	2004/01/26 14:48
		control switch)	US-PGPUB;	
1		,	EPO; JPO;	
[DERWENT;	
			IBM_TDB	
-	34	((TAP core control logic) with (TAP control register\$!) with (TAF		2004/01/26 14:49
		control switch)) with non-core	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		40 144 14	IBM_TDB	
-	3	(multi-core processor\$1) and (((core\$1 or (processor core\$1))	USPAT;	2004/01/26 14:52
		with (TAP controller\$1)) with (((TAP core control logic) with (TAP control registed)) with (TAP control registed)		
		control register\$!) with (TAP control switch)) with non-core))	EPO; JPO;	
			DERWENT;	
	1	("6115763").PN.	IBM_TDB USPAT	2004/01/26 14:52
<u> </u>	<u>_</u>	/ OTTO/OD J'EIA'	USPAT	2004/01/26 14:52